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| <u>L24</u> L19 and l9   | 0            | <u>L24</u>                   |
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| <u>L9</u>   | (711/172) [CCLS]  | 379   | <u>L9</u>  |
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| <u>L4</u>   | L3 near25 (instruction\$1 or microinstruction\$1 or macroinstruction\$1)<br>(align\$5 or combin\$6 or encod\$3 or convert\$3 or conversion\$1 or translat\$5 or<br>split\$3 or partition\$7 or divid\$4) near8 (32 or 16 or sixteen or thirty near1 two<br>or 64 or sixty or 126 or 512 or byte\$1) near15 (variable or different) near5<br>(length or size or width) | 355   | <u>L4</u>  |
| <u>L3</u>   | (align\$5 or combin\$6 or decod\$3 or encod\$3 or convert\$3 or conversion\$1 or<br>translat\$5 or split\$3 or partition\$7 or divid\$4) near8 (32 or 16 or sixteen or<br>thirty near1 two or 64 or sixty or 126 or 512 or byte\$1) near15 (variable or<br>different) near5 (length or size or width)   | 3591  | <u>L3</u>  |
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1. **Selective guarded execution using profiling on a dynamically scheduled processor**  
 Mantripragada, S.; Nicolau, A.;  
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